# Sample Exam Compiler Construction

Summer 2023

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Main exam: 22 June / Resit: 7 July 2023

Seat

Do not open this exam booklet before we ask you to. Do read this page carefully.

You can only write the exam at the seat allocated for you, and you must use the exam booklet that carries your name and student number.

This is a closed-book exam. Leave bags and jackets out of reach. Turn off all electronic devices and leave them with your bag. You may only take writing utensils, drinks, food, and your student or identity card to your seat. Please have your student or identity card clearly visible on your table.

Leaving your table or the room with your exam booklet is regarded as an attempt of deception. You may not leave the room during the first 30 minutes and the last 15 minutes of the exam. If you need to use the restrooms, please alert the supervisor. Only one person at a time may leave for the restrooms.

Before you start, please check that your exam booklet consists of **13 pages**, sequentially numbered, on 13 pages sheets of paper, and contains questions 1 through 7 b). This is page 1.

Write your solutions on the (printed) right pages of the exam booklet, in the space provided below the respective questions. Solutions written in a language other than English, in red or similar colours, on the first or last page of the booklet, on the (blank) left pages, or on additional sheets not referenced from the booklet, will not be graded. Should you run out of space, ask the supervisor for an additional sheet of paper. You may use a pencil.

The duration of the exam is **120 minutes**. The total number of points of the questions in this exam is 120. To pass the exam, 60 points will be sufficient.

Have fun!

1	2	3	4	5	6	7
6	15	25	9	16	38	11

Sum	
120	

## Question 1. True or False? (6 points)

For each of the following statements, decide whether it is true or false. If a statement is false, very briefly (i.e. in at most one sentence) correct it or explain why it is false.

a) Deciding whether a context-free grammar is ambiguous is an NP-hard problem.

b) 2-address code linear IRs are no longer in common use today.

c) When using a global display for addressability, storing the display in global memory works well for multi-threaded programs.

#### Question 2. Scanning (15 points)

Consider a language over the alphabet  $\{a,b,c\}$  with two token types defined by regular expressions as follows:

- 1. Token type SHORT with regular expression ab
- 2. Token type LONG with regular expression (ab)\*c
- a) Draw one DFA that recognises tokens of both types. Mark each accepting state with a double outline ② and annotate it with the corresponding token type. (8 points)

(You do not need to draw edges for invalid inputs: we assume that input characters that do not have an edge lead to a non-accepting invalid token state.)

Now assume we have embedded an implementation of such a DFA into a scanner that repeatedly calls function NextWord (as in the lectures and the book, repeated below in three columns) until it reaches the end of the input or finds an invalid token.

```
NextWord()
                                      if state \in S_A
                                                                             state \neq bad) do
  state \leftarrow s_0;
                                           then clear stack;
                                                                        state \leftarrow pop();
  1 exeme ← "":
                                                                        truncate lexeme;
                                      push(state);
  clear stack;
                                                                        RollBack();
                                      cat \leftarrow CharCat[char];
  push(bad);
                                                                      end;
                                      state \leftarrow \delta[state.cat]:
  while (state\neq s_e) do
                                    end;
                                                                      if state \in S_A
    NextChar(char);
                                                                        then return Type[state];
                                    while(state \notin S_A and
    lexeme ← lexeme + char;
                                                                        else return invalid;
```

- b) For each of the following inputs, state whether it is fully scanned successfully. If yes, what are the resulting tokens and their token types? (7 points)
  - (i) ababcab
  - (ii) cbabab
  - (iii) ababab

## Question 3. Parsing (25 points)

The following grammar, in ANTLR-like notation, is for expressions with binary addition (+) and subtraction (-) operators over single-letter identifiers of token type NAME. The start symbol is expr.

- a) What is the associativity of the two operators according to this grammar? Mark the correct answer. (1 point)
  - $\hfill\Box$  + and are right-associative.
  - $\square$  + and are left-associative.
- b) Is the grammar LL(1)? Briefly but precisely say why or why not. (3 points)

c) Draw the parse tree for the expression "(a + b) - c". (6 points)

Now consider the following grammar in BNF, where uppercase letters are nonterminals and lowercase latin letters are terminals, and A is the start symbol:

$$\begin{array}{ccc} A & \rightarrow B & C \\ & \mid & a \\ B & \rightarrow C & b \\ & \mid & \epsilon \\ C & \rightarrow c \\ & \mid & \epsilon \end{array}$$

d) Give the FIRST and FOLLOW sets for all nonterminals, and the FIRST+ sets for all rules. (12 points)

$$FIRST(A) = \{$$

$$FIRST(B) = \{$$

$$FIRST(C) = \{$$

$$FOLLOW(A) = \{$$

$$FOLLOW(B) = \{$$

$$FOLLOW(C) = \{$$

$$FIRST+(A \rightarrow B \ C) = \{$$

$$FIRST+(A \rightarrow a) = \{$$

$$FIRST+(B \to C \ b) = \{$$

$$FIRST+(B \to \epsilon) = \{$$

$$FIRST+(C \rightarrow c) = \{$$

$$FIRST+(C \to \epsilon) = \{$$

e) Is the grammar LL(1)? Briefly but precisely say why or why not. (3 points)

#### Question 4. Elaboration (9 points)

The following grammar in ANTLR-like notation with start symbol number defines a language for non-negative binary numbers:

We want to compute the decimal values of such numbers; for example, the decimal value of 0b0101 is 5.

a) We use one attribute decVal for all nonterminals. Below, for each grammar rule, give an attribute rule so that number. decVal is computed as the decimal value of the number. (6 points)

 $Grammar\ rule$  Attribute rule number: 'Ob' list list<sub>1</sub>: list<sub>2</sub> bit

bit: '0'

1'1'

b) Are your rules synthesised or inherited? If you have both types of rules, say which of them are synthesised and which are inherited. Briefly justify your answer.

(3 points)

#### Question 5. Graph-Based IRs (16 points)

Consider the following Java-like code snippet, which finds the maximum element  $\geq 0$  contained in array **a**:

```
1 int max = 0;
2 int i = 0;
3 while(i < a.length) {
4    if(a[i] > max)
5       max = a[i];
6    i = i + 1;
7 }
8 print("Max:", max);
```

a) Draw the control flow graph of the code snippet. Use the line numbers as the nodes of your graph, ignoring line 7. (7 points)

b) List all basic blocks of the snippet's control flow graph that consist of more than one node. (2 points)

We recall the code snippet from the previous page:

```
1 int max = 0;
2 int i = 0;
3 while(i < a.length) {
4   if(a[i] > max)
5    max = a[i];
6   i = i + 1;
7 }
8 print("Max:", max);
```

c) Draw the data dependence graph for the code snippet. Again use the relevant line numbers as nodes, and add a node for the array a. (7 points)

#### Question 6. Procedures (38 points)

Consider the Pascal program below on the left, in which only procedure calls and variable declarations are shown:

```
program Main;
2
     var x, y, z: integer;
3
     procedure A;
        var x: integer;
4
       begin { body A } end;
5
     procedure B;
6
7
       var y: real;
        procedure C;
8
          var z: real;
9
          procedure D;
10
            var y: real;
11
            begin {body D }
12
              Α;
13
            end;
14
          begin { body C }
15
            A; D;
16
          end;
17
        begin { body B }
18
          C;
19
        end;
20
     begin { Main }
21
       В;
22
     end.
23
```

a) Fill the following static coordinate table for the program. (8 points)

	1	1	· 1
scope	х	у	z
main			
A			
В			
С			
D			

b) Draw the program's scope and call graphs.

(8 points)

Scope graph:

Call graph:

We continue with following Pascal program:

```
program Main;
     var x: integer = 3, y: integer = 4, z: integer = 5;
2
     function A(r: integer): integer;
3
       var x: integer = 6;
4
       begin A := r + x + y; end;
5
     procedure B(x: integer);
6
       begin { body B }
7
         A(x + 1);
8
       end;
9
     begin { Main }
10
       B(1);
11
     end.
12
```

c) Draw a graph (as in the lectures) that visualises the structure and contents of the activation records (ARs), including the values of variables, at the following point in the program's execution: In the implementation of line 5, A is about to perform the jump back to the return address.

Annotate the ARs' contents (e.g. "return address", "return value", etc.). Draw pointers as arrows. Omit saved registers, and use static links to implement addressability. Write "—" for uninitialised or unknown values. For return addresses, use the line number of the call in the caller's code. Assume a memory-to-memory model and heap-based allocation of ARs.

(11 points)

We recall the Pascal program from the previous page:

```
program Main;
     var x: integer = 3, y: integer = 4, z: integer = 5;
2
     function A(r: integer): integer;
3
       var x: integer = 6;
4
       begin A := r + x + y; end;
5
6
     procedure B(x: integer);
       begin { body B }
7
         A(x + 1);
8
       end;
9
     begin { Main }
10
       B(1);
11
     end.
12
```

d) Write ILOC code implementing procedure B using a memory-to-memory model, heap-based allocation of ARs, and static links. Assume that register r\_hp points to an area of memory to allocate A's AR in (over increasing memory addresses), that the ILOC code implementing A has label code\_A, and that the callees restore r\_arp before returning. Do not use symbolic constants for memory offsets—use concrete values. Ignore register saving. Use only the ILOC instructions listed on the last page of this exam booklet.

(11 points)

(This question may be slightly too complex/time-consuming to appear in an exam, but it is good practice for similar but simpler/shorter questions that could appear.)

#### Question 7. Optimisations (11 points)

The following ILOC code implements an assignment involving variables a, b, and c:

```
//(1-3)
loadAI
        r_arp,
                @a
add
        r_1,
                r_1 => r_1
                                        //(4-4)
                                        //(5-7)
loadAI
                @b
                    => r_2
        r_arp,
                                        //(8-9)
mult
        r_1,
                r_2 \implies r_1
                                       //(10-12)
loadAI
        r_arp,
                @c
                    => r_2
                                        //(13-14)
mult
        r_1,
                r_2 \implies r_1
storeAI r_1
                                        //(15-17)
                    => r_arp,
                                @a
```

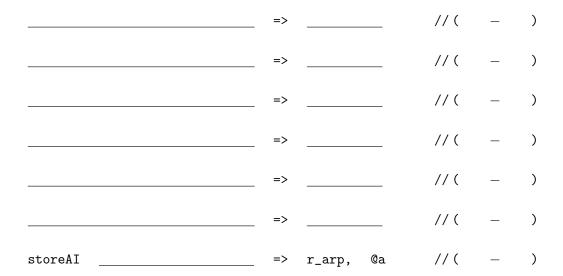
a) In Java-like syntax, what is the assignment implemented by the ILOC code?

(3 points)

Let us assume that the ILOC code is executed on an idealised pipelined CPU. On this CPU, at most one instruction can be started in each clock cycle. Instructions loadAI and storeAI complete in 3 clock cycles, mult completes in 2 clock cycles, and add completes in 1 clock cycle. An instruction that reads from register r can only start after any previous instruction writing to r is finished. We have annotated the ILOC code above with its execution timing.

b) Reorder the ILOC instructions to implement the same assignment, but take as few clock cycles as possible to execute on the pipelined CPU. You may use different and additional registers. Annotate your optimised code with its execution timing.

(8 points)



(An ILOC instruction reference table is included on the last page of this exam booklet.)

# **ILOC** Reference

$\mathbf{Opcode}$	Sources	Targets	Meaning					
Memory operat	Memory operations							
loadI	$c_1$	$r_2$	$c_1 \Rightarrow r_2$					
load	$r_1$	$r_2$	$\text{MEMORY}(r_1) \Rightarrow r_2$					
loadAI	$r_1, c_2$	$r_3$	$Memory(r_1 + c_2) \Rightarrow r_3$					
loadAO	$r_1, r_2$	$r_3$	$\text{MEMORY}(\mathbf{r}_1 + \mathbf{r}_2) \Rightarrow \mathbf{r}_3$					
store	$r_1$	$r_2$	$r_1 \Rightarrow Memory(r_2)$					
storeAI	$r_1$	$r_2, c_3$	$r_1 \Rightarrow Memory(r_2 + c_3)$					
storeA0	$r_1$	$r_2,r_3$	$r_1 \Rightarrow Memory(r_2 + r_3)$					
Arithmetic								
add	$r_1, r_2$	$r_3$	$r_1 + r_2 \Rightarrow r_3$					
addI	$r_1, c_2$	$r_3$	$r_1+c_2 \Rightarrow r_3$					
sub	$r_1, r_2$	$r_3$	$r_1 - r_2 \Rightarrow r_3$					
subI	$r_1, c_2$	$r_3$	$r_1 - c_2 \Rightarrow r_3$					
mult	$r_1, r_2$	$r_3$	$r_1 * r_2 \Rightarrow r_3$					
multI	$\mathtt{r}_1,\mathtt{c}_2$	$r_3$	$r_1 * c_2 \Rightarrow r_3$					
Control flow								
$\mathtt{cmp} \_LT$	$r_1, r_2$	$r_3$	$\texttt{true} \Rightarrow \texttt{r}_3$	$\mathrm{if} \; r_1 < r_2$				
			$\mathtt{false} \Rightarrow \mathtt{r_3}$	otherwise				
$\mathtt{cmp}\mathtt{LE}$	$\mathtt{r_1},\mathtt{r_2}$	$r_3$	$\texttt{true} \Rightarrow \texttt{r}_3$	$\mathrm{if}\ \mathtt{r_1} \leq \mathtt{r_2}$				
			$\mathtt{false} \Rightarrow \mathtt{r}_3$	otherwise				
$\mathtt{cmp\_GT}$	$\mathtt{r_1},\mathtt{r_2}$	$r_3$	$\texttt{true} \Rightarrow \texttt{r}_3$	$ \text{if } r_1 > r_2$				
			$\mathtt{false} \Rightarrow \mathtt{r_3}$					
cmp_GE	$\mathtt{r_1},\mathtt{r_2}$	r <sub>3</sub>	$\texttt{true} \Rightarrow \texttt{r}_3$	if $r_1 \geq r_2$				
			$\mathtt{false} \Rightarrow r_3$					
cmp_EQ	$r_1, r_2$	$r_3$	$true \Rightarrow r_3$	if $\mathbf{r}_1 = \mathbf{r}_2$				
			$\texttt{false}\Rightarrow \mathtt{r}_3$					
cmp_NE	$r_1, r_2$	r <sub>3</sub>	$true \Rightarrow r_3$	if $\mathbf{r}_1 \neq \mathbf{r}_2$				
a.ba	_	1 1	$false \Rightarrow r_3$					
cbr	$r_1$	$1_2, 1_3$	$egin{aligned} \mathtt{l}_2 & ightarrow \mathtt{PC} \ \mathtt{l}_3 & ightarrow \mathtt{PC} \end{aligned}$	if $r_1 = true$ otherwise				
Jumps			13 -7 10	O11161 M 126				
jumpI		11	$\mathtt{l_1} \to \mathtt{PC}$					
jump	_	$r_1$	$r_1 \rightarrow PC$					
المسل		-1	-1 /10					